

In re Patent Application for:  
**GUINEA**  
Serial No. **Not yet assigned**  
Filing date: **Herewith**

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34. The method according to Claim 33 wherein each of the input clock signals is delayed equally relative to one another by a fraction equal to the period divided by a number of said plurality of circuits.

35. The method according to Claim 32 wherein outputting comprises outputting the new signal synchronously with a trailing edge of the new signal.

#### REMARKS

It is believed that all of the claims are patentable over the prior art. Accordingly, after the Examiner completes a thorough examination and finds the claims patentable, a Notice of Allowance is respectfully requested in due course. Should the Examiner determine any minor informalities that need to be addressed, he is encouraged to contact the undersigned attorney at the telephone number below.

Respectfully submitted,



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: )  
GUINEA ET AL. )  
Serial No. Not yet assigned )  
Filing Date: Herewith )  
For: SYNCHRONISM PHASE-SWITCHING )  
CIRCUIT FOR THE RECOVERY OF )  
RECEIVED DATA )

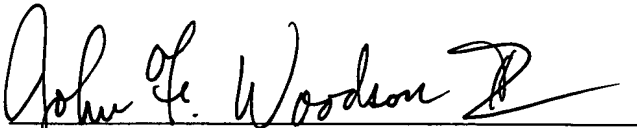
SUBMISSION OF PROPOSED MODIFICATIONS TO DRAWINGS

Director, U.S. Patent and Trademark Office  
Washington, D.C. 20231

Sir:

Submitted herewith is a request for a proposed drawing modification as indicated in red ink to label certain elements in FIGS. 1 to 3 in accordance with the specification, and also to and remove certain superfluous markings from FIGS. 1 to 5. No new matter is being added.

Respectfully submitted,



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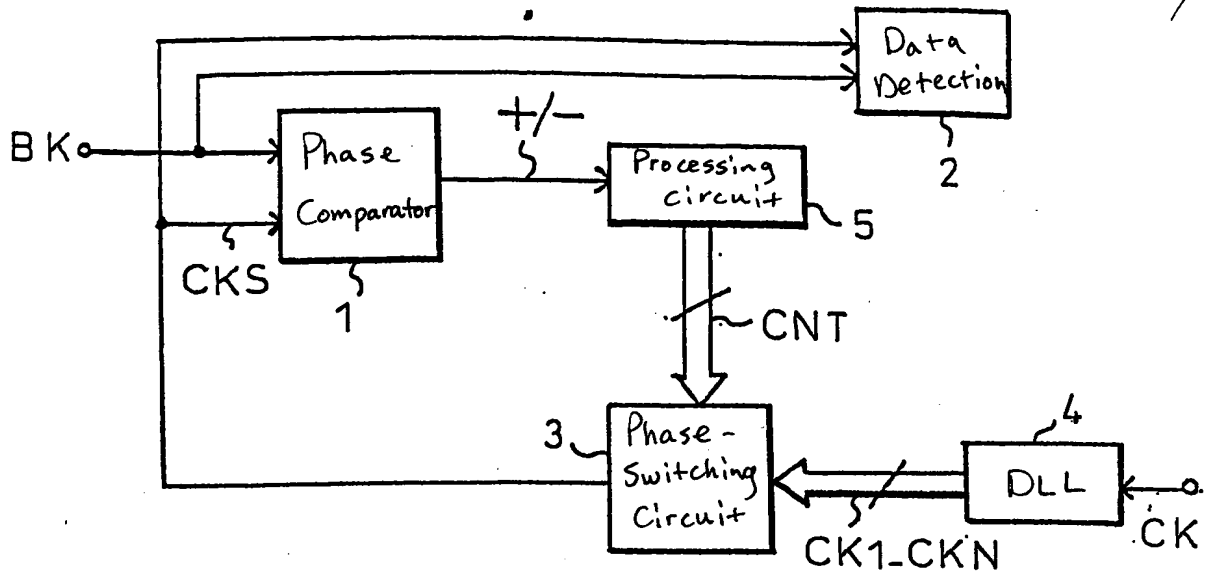


FIG. 1

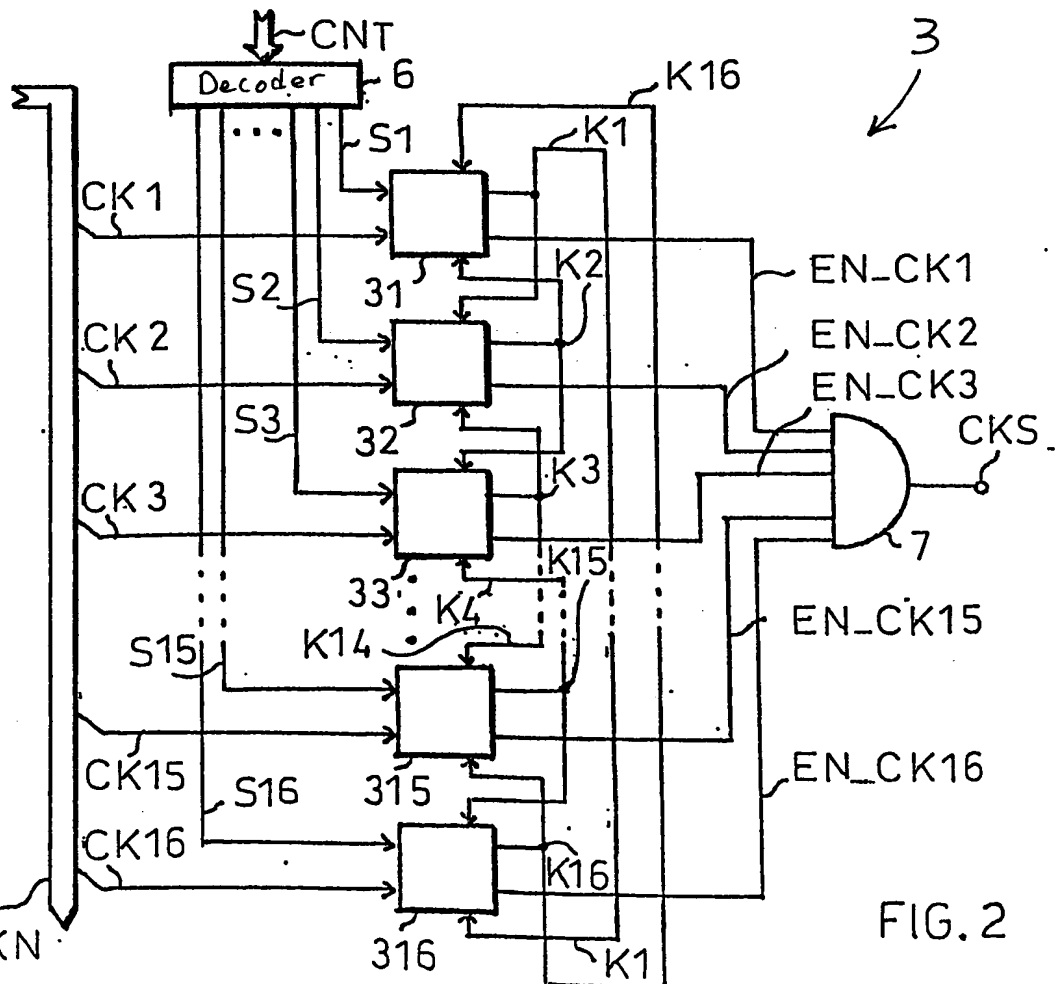


FIG. 2



FIG. 3

FIG. 4

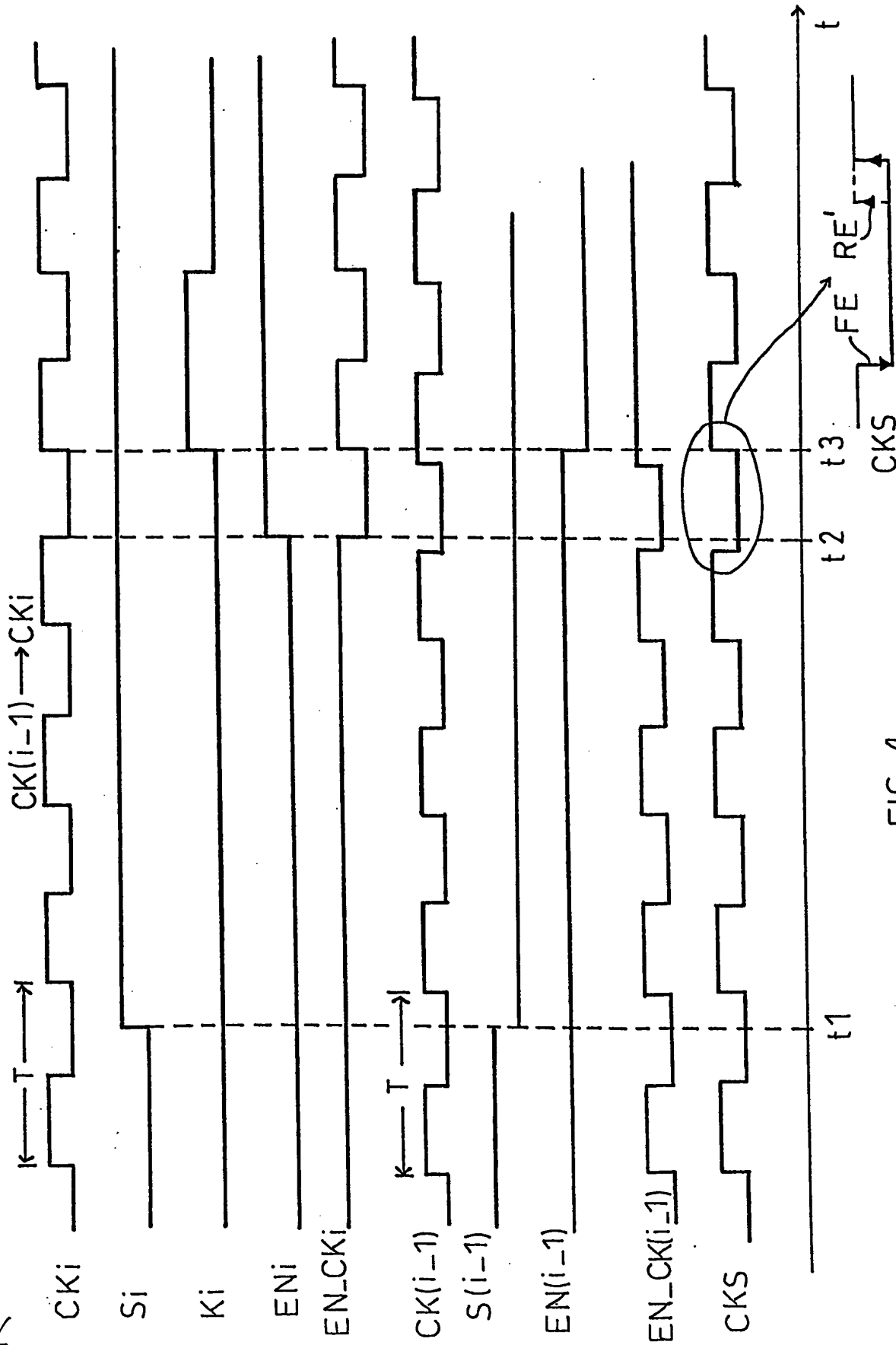


FIG. 4

$CK_i \rightarrow CK(i-1)$

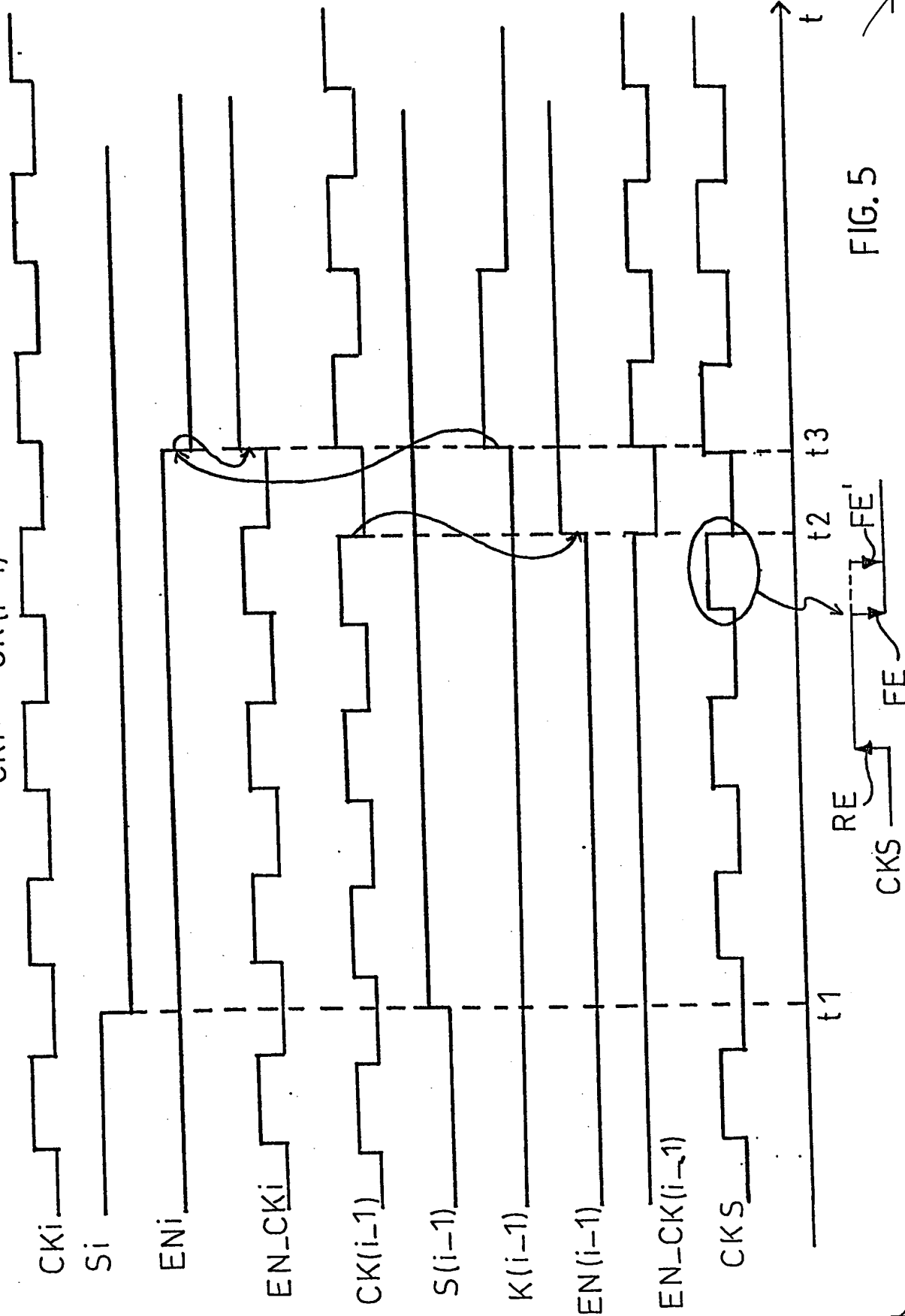


FIG. 5